

# VERILOG DIGITAL SYSTEM DESIGN BY ZAINALABEDIN NAVABI



## Verilog Digital System Design

SECOND EDITION

- ✓ Verilog 2001
- ✓ Test-bench development
- ✓ OVL Verification Library
- ✓ New synthesis standards

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Zainalabedin Navabi (Boston, MA) is Professor of Electrical and Computer Engineering at Northeastern University. He is the author of the first edition of Verilog Digital System Design.

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- Sales Rank: #2387039 in eBooks
- Published on: 2005-10-24
- Released on: 2005-10-24
- Format: Kindle eBook

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